



IP20  
Docket No.: 4444-0120P  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Chin LEE

Application No.: 10/620,456

Confirmation No.: 9179

Filed: July 17, 2003

Art Unit: 2184

For: **MEMORY MODELING CIRCUIT WITH  
FAULT TOLERATION**

Examiner: Not Yet Assigned

**STATUS INQUIRY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: **FEB 1 2006**

Respectfully submitted,

By Joe McKinney Muncy  
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